

# Graphene-Based Nanoelectronics

Frank Schwierz

Institut für Mikro- und Nanotechnologien, Technische Universität Ilmenau, PF 100565, 98684 Ilmenau, Germany  
frank.schwierz@tu-ilmenau.de

## 1. Introduction

Two pioneering papers on the 2D (two-dimensional) carbon-based material graphene published in 2004 [1-2] ignited an enormous attention to this material and it did not take long before the electron device community showed interest in graphene [3-5]. Mainly graphene's excellent carrier transport properties have attracted device engineers and led to the expectation that graphene could be an ideal material for high-performance ultra-fast transistors. Already the early papers reported on room-temperature mobilities of  $10^4$   $\text{cm}^2/\text{Vs}$  in graphene [1], and meanwhile mobilities exceeding  $10^5$   $\text{cm}^2/\text{Vs}$  at 300 K have been measured in graphene [6].

Many groups worldwide started research on graphene transistors and in 2007 the first graphene MOSFET has been presented [7]. Since then, the performance of graphene MOSFETs could continuously be improved. In spite of this progress, however, so far graphene could not fulfill the high expectations and the main problems of graphene transistors are still not solved. Meanwhile part of the early excitement is gone and the potential of graphene for electronics is assessed less enthusiastic compared to the early days [8-9].

Another interesting aspect is that, virtually in the lee of graphene, a variety of other 2D materials has been discovered that may be of interest for electronics [10].

## 2. Graphene Transistors for Digital Logic

Current digital logic is dominated by CMOS. For any FET to be used in CMOS, good switch-off is mandatory, and switching a FET off inevitably requires a semiconducting channel with a sizeable bandgap (preferably 0.4 eV or above). Here, graphene faces a serious problem. Pristine large-area graphene is gapless and behaves like a semi-metal. A gapless MOSFET channel, however, cannot be switched off and therefore large-area graphene is not useful for CMOS. An option to open a gap in graphene is forming narrow GNRs (graphene nanoribbon). GNR MOSFETs with on-off ratios exceeding  $10^6$  have successfully been fabricated [11]. However, ribbons having a width well below 10 nanometers are needed for a gap ensuring good switch-off [4, 11] and this represents a serious processing challenge. Moreover, the gap opening in GNRs is

accompanied by a dramatic mobility reduction and irregular ribbon edges deteriorate transistor performance.

Due to these problems, graphene MOSFETs for digital logic receded into the background recently and more attention is now paid to graphene RF (radio frequency) MOSFETs.

## 3. Graphene Transistors for RF Applications

RF FETs are usually operated in the on-state and do not need to switch off. Therefore, on first view, gapless large-area graphene might be a useful channel material for RF FETs. The first RF graphene MOSFETs have been reported in 2008 [12] and since that time many groups have realized graphene RF MOSFETs with large-area gapless channels. Figure 1 shows the best reported cutoff frequencies  $f_T$  (the frequency where the small-signal current gain drops to unity) of graphene MOSFETs together with the  $f_T$  performance of competing RF FET types.

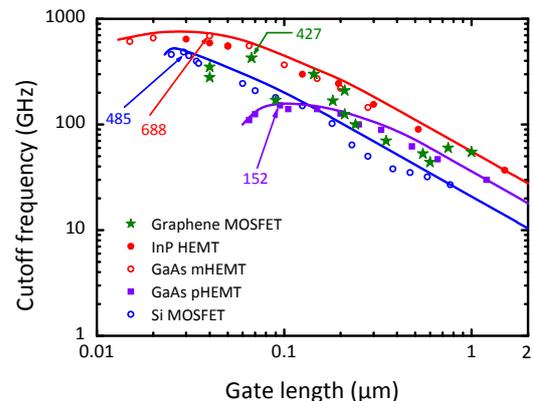


Fig. 1. Cutoff frequency of graphene MOSFETs vs. gate length, together with the  $f_T$  performance of competing RF FET types. The numbers indicate the record  $f_T$ 's in GHz. Updated from [9].

As can be seen, in terms of  $f_T$  graphene MOSFETs outperform Si MOSFETs with comparable size and compete well with InP HEMTs and GaAs mHEMTs which are the fastest RF FETs at all. The current record for graphene is a  $f_T$  of 427 GHz achieved with a 67-nm gate MOSFET [13].

For most applications, the maximum frequency of oscillation  $f_{\text{max}}$  (the frequency where the transistor's power gain drops to unity) is more important than  $f_T$ . Unfortunately, regarding  $f_{\text{max}}$  the picture looks less

promising for graphene MOSFETs. While, as shown in Fig. 2, the record  $f_{\max}$  of III-V HEMTs exceeds 1 THz and Si MOSFETs with  $f_{\max}$  of 420 GHz have been reported, the best graphene RF FETs show  $f_{\max}$  of about 100 GHz only [14]. It has been discussed in detail that the main reason for the poor  $f_{\max}$  performance of graphene MOSFETs is the unsatisfying saturation of the drain current which, in turn, is a consequence of the missing gap in large-area graphene [9]. This brings us to the conclusion that high-performance RF MOSFETs with competitive  $f_{\max}$  need a channel with a gap.

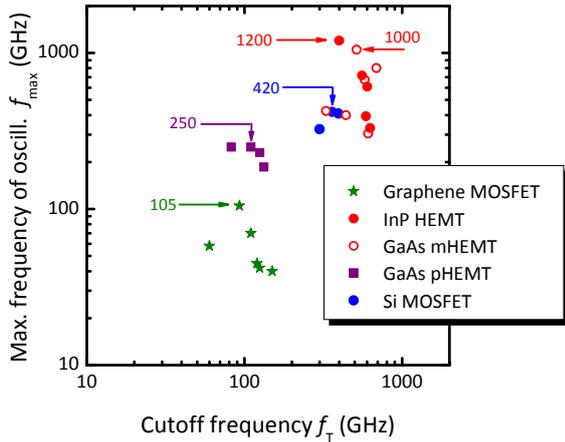


Fig. 2.  $f_{\max}$  vs.  $f_T$  of graphene MOSFETs and competing RF FET classes. Updated from [15].

#### 4. Outlook

So far we have presented a gloomy picture of the prospects of graphene in nanoelectronics. We should, however, not prematurely rule out graphene and the 2D materials beyond graphene. Instead, the following issues deserve consideration.

(i) When MOSFET scaling reaches the 5 nm gate length level, direct source-drain tunneling becomes a serious issue. Recent theoretical studies have shown that by using channel materials with heavy carrier effective mass (and thus low mobility), such as GNRs or 2D TMDs (transition metal dichalcogenide) like  $\text{MoS}_2$ , the tunneling currents can significantly be suppressed [16–17]. Thus, GNRs and 2D TMDs can possibly extend the lifetime of MOSFET scaling and Moore's Law beyond the limits of Si.

(ii) There are transistor concepts beyond the MOSFET that can be realized with graphene. For example, the vertical graphene base transistor [18] and the BiSFET (bilayer pseudospin FET) [19], for which the missing gap does not represent a problem, have been proposed.

(iii) Semiconductor electronics comprises more than merely digital logic and RF. Since graphene and other 2D materials are bendable, they show promise for the emerging field of flexible electronics [20], to name just one example.

Thus, we conclude with the optimistic remark that research on graphene and the 2D materials beyond graphene has not nearly reached its limits and will stay exciting for many years to come.

#### Acknowledgments

This work has partially been supported by an Excellence Research Grant and an Intra-Faculty Research Grant of Technische Universität Ilmenau, Germany, and by DFG under contract no. SCHW 729/16-1.

#### References

- [1] K. S. Novoselov, A. K. Geim, S. V. Morozov, D. Jiang, Y. Zhang, S. V. Dubonos, I. V. Grigorieva, and A. A. Firsov, *Science*, vol. 306, 666 (2004).
- [2] C. Berger, Z. Song, T. Li, X. Li, A. Y. Ogbazghi, R. Feng, Z. Dai, A. N. Marchenkov, E. H. Conrad, P. N. First, and W. A. de Heer, *J. Phys. Chem. B*, vol. 108, 19912 (2004).
- [3] P. Avouris, *Nano Lett.*, vol. 10, 4285 (2010).
- [4] F. Schwierz, *Nature Nanotechnol.*, vol. 5, 487 (2010).
- [5] K. Kim, J.-Y. Choi, S.-H. Cho, and H.-J. Chung, *Nature*, vol. 479, 338 (2011).
- [6] A. S. Mayorov, R. V. Gorbachev, S. V. Morozov, L. Britnell, R. Jalil, L. A. Ponomarenko, P. Blake, K. S. Novoselov, K. Watanabe, T. Taniguchi, and A. K. Geim, *Nano Lett.*, vol. 11, 2396 (2011).
- [7] M. C. Lemme, T. J. Echtermeyer, M. Baus, and H. Kurz, *IEEE Electron Device Lett.*, vol. 28, 282 (2007).
- [8] M. Segal, *Nature*, vol. 483, S43 (2012).
- [9] F. Schwierz, *Proc. IEEE*, vol. 101, 1567 (2013).
- [10] K. J. Koski and Y. Cui, *ACS Nano*, vol. 7, 3739 (2013).
- [11] X. Li, X. Wang, L. Zhang, S. Lee, and H. Dai, *Science*, vol. 319, 1229 (2008).
- [12] I. Meric, N. Bakkitskaya, P. Kim, and K. L. Shepard, *Tech. Dig. IEDM*, 1 (2008).
- [13] R. Cheng, J. Bai, L. Liao, H. Zhou, Y. Chen, L. Liu, Y.-C. Lin, S. Jiang, Y. Huang, and X. Duan, *PNAS*, vol. 109, 11588 (2012).
- [14] Z. H. Feng, C. Yu, J. Li, Q. B. Liu, Z. Z. He, X. B. Song, J. J. Wang, and S. J. Cai, To be published.
- [15] F. Schwierz, *Nature*, vol. 472, 41 (2011).
- [16] S. S. Sylvia, H.-H. Park, A. B. Khayer, K. Alam, G. Klimeck, and R. K. Lake, *IEEE Trans. Electron Devices*, vol. 59, 2064 (2012).
- [17] K. Majumdar, C. Hobbs, and P. D. Kirsch, *IEEE Electron Device Lett.*, vol. 35, 402 (2014).
- [18] W. Mehr, J. Dabrowski, J. C. Scheytt, G. Lippert, Y.-H. Xie, M. C. Lemme, M. Ostling, and G. Lupina, *IEEE Electron Device Lett.*, vol. 33, 691 (2012).
- [19] S. K. Banerjee, L. F. Register, E. Tutuc, D. Reddy, and A. H. MacDonald, *IEEE Electron Device Lett.*, vol. 30, 158 (2009).
- [20] J. Lee, H.-Y. Chang, T.-J. Ha, H. Li, R. S. Ruoff, A. Dodabalapur, and D. Akinwande, *Tech. Dig. IEDM*, 491 (2013).